## FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- 16:8 multiplexer function with zero delay
- $5 \Omega$ bidirectional switches connect inputs to outputs
- Zero propagation delay, zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- Direct bidirectional connection for mux, demux
- $25 \Omega$ resistors for low noise
- Available in QSOP package


## DESCRIPTION:

The QS32390 provides a 16:8 multiplexer logic switch. The QS32390 has an internal $25 \Omega$ resistor to reduce reflection noise in high-speed applications. The enable inputs connect one of two inputs to the common I/O pin, respectively. The multiplexer function can be used to select and route logic signals for zero delay, isolate bus capacitance, form crossbar switches, etc.

Mux/Demux devices provide an order of magnitude faster speed than equivalent logic devices.

The QS32390 is characterized for operation at $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$.

## APPLICATIONS:

- Video, audio, graphics switching, muxing
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)

FUNCTIONAL BLOCK DIAGRAM


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## PIN CONFIGURATION



QSOP TOP VIEW

## ABSOLUTE MAXIMUM RATINGS(1)

| Symbol | Description | Max | Unit |
| :--- | :--- | :---: | :---: |
| VTERM $^{(2)}$ | Supply Voltage to Ground | -0.5 to +7 | V |
| Vterm $^{(3)}$ | DC Switch Voltage Vs | -0.5 to +7 | V |
| VTERM $^{(3)}$ | DC Input Voltage VIN | -0.5 to +7 | V |
| VAC | AC Input Voltage (pulse width $\leq 20 \mathrm{~ns})$ | -3 | V |
| IOUT | DC Output Current | 120 | mA |
| Pmax | Maximum Power Dissipation $\left(\mathrm{TA}=85^{\circ} \mathrm{C}\right)$ | 0.5 | W |
| TstG | Storage Temperature | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

## NOTE:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc terminals.
3. All terminals except Vcc.

## CAPACITANCE

$\left(\mathrm{T} A=+25^{\circ} \mathrm{C}, \mathrm{f}=1.0 \mathrm{MHz}, \mathrm{VIN}=0 \mathrm{~V}\right.$, Vout $\left.=0 \mathrm{~V}\right)$

| Pins |  | Typ. | Max. ${ }^{(1)}$ | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Control Pins |  | 4 | 5 | pF |
| QuickswitchChannels <br> (Switch OFF) | Demux | 5 | 7 | pF |
|  | Mux | 9 | 10 |  |

NOTE:

1. This parameter is measured at characterization but not tested.

## PIN DESCRIPTION

| Pin Names | $1 / 0$ | Description |
| :---: | :---: | :--- |
| A0 - A9 | $1 / 0$ | Bus A |
| Bo - B9 | $\mathrm{I} / \mathrm{O}$ | Bus B |
| $\overline{\mathrm{AEN}}, \overline{\mathrm{BEN}}$ | I | Bus Switch Enable |

FUNCTION TABLE ${ }^{(1)}$

| $\overline{\text { AEN }}$ | $\overline{\mathrm{BEN}}$ | A0-A9 | B0-B9 | Function |
| :---: | :---: | :---: | :---: | :---: |
| $H$ | $H$ | Off | Off | Disconnect |
| $L$ | $H$ | On | Off | $A$ to $A B$ |
| $H$ | $L$ | Off | On | B to $A B$ |
| $L$ | $L$ | On | On | $A, B$ to $A B$ |

## NOTE:

1. $\mathrm{H}=\mathrm{HIGH}$ Voltage Level

L = LOW Voltage Level

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE
Following Conditions Apply Unless Otherwise Specified:
Industrial: $\mathrm{TA}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{VCC}=5.0 \mathrm{~V} \pm 5 \%$

| Symbol | Parameter | Test Conditions | Min. | Typ. ${ }^{11}$ | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| VIH | Input HIGH Level | Guaranteed Logic HIGH for Control Pins | 2 | - | - | V |
| VIL | Input LOW Level | Guaranteed Logic LOW for Control Pins | - | - | 0.8 | V |
| In | Input LeakageCurrent (Control Inputs) ${ }^{2}$ | $\mathrm{OV} \leq \mathrm{VIN} \leq \mathrm{Vcc}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| Ioz | Off-State Output Current (Hi-Z) | OV $\leq$ Vout $\leq$ Vcc, Switches OFF | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| Ron(3) | Switch ON Resistance | $\mathrm{VcC}=\mathrm{Min}$., VIN $=0 \mathrm{~V}$, Ion $=30 \mathrm{~mA}$ | 15 | 20 | 35 | $\Omega$ |
|  |  | $\mathrm{VCC}=\mathrm{Min} ., \mathrm{V} \mathrm{V}=2.4 \mathrm{~V}$, Ion $=15 \mathrm{~mA}$ | 15 | 19 | 40 |  |
| Vp | Pass Voltage ${ }^{(2)}$ | $\mathrm{VIN}=\mathrm{VCC}=5 \mathrm{~V}$, IOUT $=-5 \mu \mathrm{~A}$ | 3.7 | 4 | 4.2 | V |

NOTES:

1. Typical values are at $\mathrm{VCC}=5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.
2. Pass Voltage is guaranteed but not production tested.
3. Rout changed on March 8, 2002. See rear page for more information.

TYPICAL ON RESISTANCE vs Vin AT Vcc $=5 \mathrm{~V}$


## POWER SUPPLY CHARACTERISTICS

| Symbol | Parameter | Test Conditions ${ }^{(1)}$ | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: |
| ICCQ | Quiescent Power Supply Current | Vcc = Max., VIN $=$ GND or Vcc, $f=0$ | 3 | $\mu \mathrm{A}$ |
| $\Delta \mathrm{lcC}$ | Power Supply Current per Control Input HIGH ${ }^{(2)}$ | $\mathrm{Vcc}=$ Max., $\mathrm{VIN}=3.4 \mathrm{~V}, \mathrm{f}=0$ | 1.5 | mA |
| ICCD | Dynamic Power Supply Current per MHz ${ }^{(3)}$ | Vcc = Max., A, B, and AB pins open Control Inputs Toggling at 50\% Duty Cycle | 0.25 | $\mathrm{mA} / \mathrm{MHz}$ |

## NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per TLL driven input ( $\mathrm{V} \mathbb{I N}=3.4 \mathrm{~V}$, control inputs only). $\mathrm{A}, \mathrm{B}$, and AB pins do not contribute to $\Delta \mathrm{lcc}$.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The $\mathrm{A}, \mathrm{B}$, and $\mathrm{AB} \mathrm{I} / \mathrm{O}$ s generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

## SWITCHING CHARACTERISTICS OVER OPERATING RANGE

$\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{Vcc}=5.0 \mathrm{~V} \pm 5 \%$;
Cload $=50 p F$, Rload $=500 \Omega$ unless otherwise noted.

| Symbol | Parameter | Min. ${ }^{(1)}$ | Typ. | Max. | Unit |
| :---: | :--- | :---: | :---: | :---: | :---: |
| tPLH <br> tPHL | DataPropagationDelay ${ }^{(2,4)}$ | AxBx to CxDx, CxDx to AxBx | - | - | $1.25^{(3)}$ |
| tPZH | Switch Turn-onDelay | 1.5 | - | 7.5 | ns |
| tPZL | $\overline{B E}$ to Ax, Bx, Cx, Dx |  |  |  |  |
| tPHZ <br> tPLZ | Switch Turn-offDelay <br> $(2)$ <br> $\overline{B E}$ <br> to Ax, Bx, Cx, Dx | - | - | 5.5 | ns |

NOTES:

1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The time constant for the switch alone is of the order of 1.25 ns for $\mathrm{CL}=50 \mathrm{pF}$.
4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## ORDERINGINFORMATION



Quarter Size Outline Package QSOP - Green

High Speed CMOS QuickSwitch 16:8 Multiplexer

As per PCN L0201-02, the Output Resistance (Ron) specifications have changed as of March 8, 2002. The original specifications were:

| Parameter | Description | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| RoN | $\mathrm{VCC}=\mathrm{Min}, \mathrm{VIN}=0 \mathrm{~V}, \mathrm{ION}=30 \mathrm{~mA}$ | 20 | 28 | 40 | $\Omega$ |
|  | $\mathrm{VCC}=\mathrm{Min}, \mathrm{VIN}=2.4 \mathrm{~V}, \mathrm{ION}=15 \mathrm{~mA}$ | 24 | 35 | 48 |  |

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